



Our Docket No: 042390.P1139

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

3/a
11-7-01
D. Bell

In re Application of:)
Boock et al.) Examiner: Not yet assigned
Application No: 09/896,378) Art Unit: Not yet assigned
Filed: June 28, 2001)
For: Method and Apparatus for Adapting)
to a Clock Rate Transition in a)
Communications Network Using Idles)

FIRST CLASS CERTIFICATE OF MAILING
(37 C.F.R. § 1.8 (a))

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On August 9, 2001 Date of Deposit

April Worley Name of Person Mailing Correspondence
April Worley Signature 8/9/01 Date

Box Non-Fee Amendment
Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Prior to examination of the above-captioned case, Applicants respectfully request the Examiner to enter the following amendment and to consider the following remark:

In the Specification:

Please delete paragraph 13 as filed, and replace with the following paragraph: ✓

[0013] The PCS data path can be considered as having a tx (transmit) path from the XGXS to the PMA and a rx (receive) path from the PMA to the XGXS. As shown in Figure 2, in a tx-path, the PCS interface receives a data stream 69 in XGMII (10 Gigabit Media Independent Interface) format. This format has four eight-bit lanes with each bit double clocked to achieve a 312.5 Mbit/s data rate on 156.25 MHz lines. The data lanes can be consolidated and then can be divided into pairs of single data rate lines, as shown. The single